

EAST Search History + Inventor + Interference

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	966566	buffer	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:08
L2	35328	buffer adj circuit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:08
L3	12232	2 and inverter	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:08
L4	9927	3 and transistors	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:09
L5	4285	4 and capacitance	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:12
L6	1265	5 and power adj source	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:13
L7	2770	5 and power adj supply	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:13

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L8	3213	6 or 7	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:13
L9	641	8 and first adj transistor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:14
L10	594	9 and second adj transistor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:14
L11	369	10 and third adj transistor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:14
L12	274	11 and fourth adj transistor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:14
L13	167	12 and fifth adj transistor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:14
L14	125	13 and sixth adj transistor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:15

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L15	46	14 and second adj power adj (source or supply)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:16
L16	15	15 and first adj signal	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:26
L17	1	16 and output adj end	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:17
L18	257	11 and driving	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:27
L19	25	18 and bootstrap	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:28
L20	2	19 and float	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:29
L21	49	18 and float\$4 near3 gate	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:31

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L22	1	18 and (float\$4 near3 gate) near3 second adj transistor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:32
L23	1	18 and (float\$4 near3 gate) near3 (second adj transistor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:32
L24	1	18 and (float\$4 near3 gate) with (second adj transistor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:33
L25	18	18 and (turn\$4 adj off) with (first adj transistor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:33
L26	1	25 and (float\$4 near3 gate) with (second adj transistor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:34
L27	1	25 and bootstrap	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:34
L28	0	(buffer adj circuit and first adj transistor and first adj power adj source and first adj node and second adj transistor and second adj power adj source and inverse and third adj transistor and second adj node and fourth adj transistor and first adj signal and fifth adj transistor and output adj end and sixth adj transistor and capacitance).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:42

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L29	0	(buffer adj circuit and first adj transistor and first adj power adj source and output adj end and second adj transistor and second adj power adj source and capacitance adj driving adj circuit and third adj transistor and first adj signal and float and bootstrap).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:44
L30	103	shin and dong-yong	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:46
L32	2	30 and buffer adj circuit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:48
L33	104027	chung, bo-yong	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:47
L35	18	chung and bo-yong	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:47
L36	2	35 and buffer adj circuit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 00:48